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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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EXAMINER

ZHOU, TING

ART UNIT	PAPER NUMBER
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2173

DATE MAILED: 03/18/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/083,229

Applicant(s)

TRAN ET AL.

Examiner

Ting Zhou

Art Unit

2173

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 31 January 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

### DETAILED ACTION

1. The amendment filed on 31 January 2005 have been received and entered. Claims 1-20 as amended are pending in the application.

#### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 1-6, 8-10 and 13-20 are rejected under 35 U.S.C. 102(e) as being anticipated by McDonald et al. U.S. Patent 6,530,065.

Referring to claims 1 and 15, McDonald et al. teach a computer implemented method and program comprising segmenting the display into a plurality of regions (as shown in Figures 8A and 17, the display screen of the interface is segmented into a plurality of sections; for example,

Art Unit: 2173

in Figure 17, the display contains a region for displaying controls such as “Choose a Part”, “Analyze a Design”, etc., and a region for “Components”) (column 14, lines 61-67 through column 15, lines 1-10), displaying, in a first of the plurality of regions, information associated with a netlist as a plurality of cell names, with the cells names being listed in a listing sequence and corresponding to the plurality of electrical functions (displaying a list of electrical circuit parts used to create a design, with the parts listed in alphabetical order according to the part name; furthermore, an alternate list of parts that can be used and displayed on the screen if the user selects the “Select Alternate Part” button) (column 14, lines 61-67 through column 15, lines 1-10 and further shown in Figures 16A and 17), displaying in a second of the plurality of regions, a plurality of virtual buttons (GUI selectable icons/tabs such as “Choose a Part”, “Create a Design”, etc.) (Figure 17), a subgroup of which operates to commence testing of parameters of a set of the plurality of electrical functions, with the electrical functions included in the set being associated with a group of the plurality of cell names (controls such as “Analyze a Design” and the “Simulate” button allow users to test the designed circuit with the included parts and parameters) (column 8, lines 39-48 and Figure 17), and identifying, among the plurality of cell names, cell names affiliated with the group (selected parts/components of the circuit to be simulated are displayed in the “Components” Sections in Figure 17 and also displayed on the schematic shown in Figure 19) (column 14, lines 61-67 through column 15, lines 1-10).

Referring to claim 10, McDonald et al. teach a computer network comprising a server storing a netlist (column 5, lines 17-37 and further shown in Figure 1A), a client terminal (client’s computer with a web browser displaying the webpage) (column 7, lines 1-20 and further shown in Figure 1B) having a display segmented into a plurality of regions (as shown in Figures

Art Unit: 2173

8A and 17, the display screen of the interface is segmented into a plurality of sections; for example, in Figure 17, the display contains a region for displaying controls such as “Choose a Part”, “Analyze a Design”, etc., and a region for “Components”) (column 14, lines 61-67 through column 15, lines 1-10), with information, associated with a netlist, being displayed in a first of the plurality of regions, as a plurality of cell names, the cells names being ordered in a listing sequence and corresponding to the plurality of electrical functions (displaying a list of electrical circuit parts used to create a design, with the parts listed in alphabetical order according to the part name; furthermore, an alternate list of parts that can be used and displayed on the screen if the user selects the “Select Alternate Part” button) (column 14, lines 61-67 through column 15, lines 1-10 and further shown in Figures 16A and 17), and a plurality of virtual buttons being displayed in a second of the plurality of regions (GUI selectable icons/tabs such as “Choose a Part”, “Create a Design”, etc.) (Figure 17), a subset of which operates to commence testing of parameters of a set of the plurality of electrical functions, with the electrical functions included in the set being associated with a group of the plurality of cell names (controls such as “Analyze a Design” and the “Simulate” button allow users to test the designed circuit with the included parts and parameters) (column 8, lines 39-48 and Figure 17), and the plurality of cell names affiliated with the group having a visually perceivable identifier associated therewith (selected parts/components of the circuit to be simulated are displayed in the “Components” Section in Figure 17 and each component listed has associated therewith, a visual outline of the component, shown via the visual indicators displayed under the “Thermally Modelled” column) (column 14, lines 61-67 through column 15, lines 1-10).

Art Unit: 2173

Referring to claims 2 and 16, McDonald et al. teach selectively associating individual cell names with the group by effectuating a cursor event with respect to one of the plurality of virtual buttons and identifying among the plurality of cell names, cell names associated with the set of the plurality of electrical functions (users can associate parts/components with a circuit to be designed and simulated by using cursor events such as selecting the button “Select Alternate Part” and consequently selecting a part in the displayed list) (column 13, lines 51-67 and Figure 12).

Referring to claims 3 and 17, McDonald et al. teach each of the plurality of cell names corresponds to an address in the sequence that differs from the address corresponding to the remaining cell names in the sequence (each cell name, or part name has a corresponding address, or part number; for example, “593D475X0050D2T” is the part number for the part “Cin”) (Figure 17) and further including affiliating cell names with the group by effectuating a cursor event with respect to one of the plurality of virtual buttons and identifying among the plurality of cell names, cell names that correspond to a range of addresses in the sequence associated with the set of the plurality of electrical functions (users can use buttons such as “Select Alternate Part” and consequently selecting a part with a corresponding part number to associate parts/components with the designed circuit) (column 13, lines 51-67 and Figure 12).

Referring to claims 4 and 18, McDonald et al. teach selectively affiliating all of the cell names with the group by effectuating a cursor event with respect to one of the plurality of virtual buttons and identifying all of the plurality of cell names (all of the components/parts of the circuit are tested when the user selects the “simulate” button on the interface) (column 8, lines 39-48 and column 21, lines 40-58).

Art Unit: 2173

Referring to claims 5, 13 and 19, McDonald et al. teach displaying a data entry field in a third of the plurality of regions to facilitate selecting, from multiple categories, a category of parameters of the set of the plurality of electrical functions to test (the user can input and select the desired parameters and components values to simulate; for example, the user can display a dialog box for a desired component and enter data values for the desired component and initiate a new simulation) (column 7, lines 1-20, column 8, lines 39-48 and column 11, lines 52-55).

Referring to claims 6, 14 and 20, McDonald et al. teach testing the parameters of the set of the plurality of electrical functions and displaying in a fourth of the plurality of regions information concerning results of the testing (a summary of the circuit simulation can be displayed, as shown in Figure 30, or a waveform resulting from the simulation can be displayed on the screen) (column 11, lines 52-67 through column 12, lines 1-33 and Figure 5A).

Referring to claim 8, McDonald et al. teach displaying a visually perceivable identifier with the cell names (a visual outline of the component is displayed with each part/component listed) (column 15, lines 6-10 and further shown by the visual indicators displayed under the “Thermally Modelled” column in Figure 17).

Referring to claim 9, McDonald et al. teach displaying a visually perceivable identifier with the cell name groups while maintaining the listing sequence (a visual outline of the component is displayed with each part/component listed, in the sequence each parts/component is displayed) (column 15, lines 6-10 and further shown by the visual indicators displayed under the “Thermally Modelled” column in Figure 17).

Art Unit: 2173

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over McDonald et al. U.S. Patent 6,530,065, as applied to claim 1 above, and Fan et al. U.S. Publication 2002/0188902.

Referring to claim 7, McDonald et al. teach all of the limitations as applied to claim 1 above. However, McDonald et al. fail to explicitly teach sequentially testing the parameters of the set of the plurality of electrical functions, with an order in which the electrical functions of the set are tested being independent of an order in which the cell names are arranged in the listing sequence. Fan et al. teach an interface for testing an integrated circuit with a plurality of parameters similar to that of McDonald et al. In addition, Fan et al. further teach sequentially testing the parameters of the set of the plurality of electrical functions, with an order in which the electrical functions of the set are tested being independent of an order in which the cell names are arranged in the listing sequence (the users can test the circuit sequentially with a test sequence created from selected listed parameters to associated with test patterns) (Fan et al.: page 2, paragraphs 0013-0014). It would have been obvious to one of ordinary skill in the art, having the teachings of McDonald et al. and Fan et al. before him at the time the invention was made, to modify the method for testing circuit components of McDonald et al. to include the ordered testing of circuit parameters, taught by Fan et al. One would have been motivated to



Art Unit: 2173

make such a combination in order to provide an accurate and systematic way for users to view the output results of circuit testing, thus allowing users to easily see which components caused a test error or failure.

4. Claims 11 and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over McDonald et al. U.S. Patent 6,530,065, as applied to claim 10 above, and Kekic et al. U.S. Patent 6,664,978.

Referring to claim 11, McDonald et al. teach all of the limitations as applied to claim 10 above. However, McDonald et al. fail to explicitly teach the visually perceivable identifier consists of having a region of the computer surrounding cell names, affiliated with the group, displayed in a color that differs from displayed in the region surrounding the remaining cell names of the plurality of cell names. Kekic et al. teach a client-server network comprising a graphical interface for displaying a list of components similar to that of McDonald et al. In addition, Kekic et al. further teach the visually perceivable identifier for the components consists of having a region of the computer surrounding cell names, affiliated with the group, displayed in a color that differs from displayed in the region surrounding the remaining cell names of the plurality of cell names (each component, or outline of the component of the network can be displayed in a different color to indicate a different status of the component) (Kekic et al.: column 5, lines 51-65). It would have been obvious to one of ordinary skill in the art, having the teachings of McDonald et al. and Kekic et al. before him at the time the invention was made, to modify the computer network for testing circuit components of McDonald et al. to include the display of components in different colors, taught by Kekic et al. One would have been motivated

Art Unit: 2173

to make such a combination in order to allow users to quickly and easily obtain considerable information about the status of a tested circuit component at a glance of the visual display.

Referring to claim 12, McDonald et al. teach all of the limitations as applied to claim 10 above. However, McDonald et al. fail to explicitly teach the visually perceivable identifier consists of an icon displayed adjacent to the cell names affiliated with the group. Kekic et al. teach a client-server network comprising a graphical interface for displaying a list of components similar to that of McDonald et al. In addition, Kekic et al. further teach the visually perceivable identifier consists of an icon displayed adjacent to the cell names affiliated with the group (displaying an icon with each component of the network displayed as a list of components in the navigation tree on the left hand side of the display) (Kekic et al.: column 23, lines 27-40, column 56, lines 66-67 through column 57, lines 1-2 and further shown in Figures 6A-6C). It would have been obvious to one of ordinary skill in the art, having the teachings of McDonald et al. and Kekic et al. before him at the time the invention was made, to modify the computer network for testing circuit components of McDonald et al. to include displaying an icon with each component, taught by Kekic et al. One would have been motivated to make such a combination in order to allow users to quickly and easily have access to and select components to be used as parts of a circuit design.

#### ***Response to Arguments***

5. Applicant's arguments filed 31 January 2005 have been fully considered but they are not persuasive:

Art Unit: 2173

6. With respect to claims 1 and 15, the applicant submits that McDonald shows all the components of an electrical design, i.e. the entire circuit is simulated or analyzed, and therefore fails to disclose “identifying, among the plurality of cell names, cell names affiliated with the group”. The examiner respectfully disagrees. The language of the claims do not limit the scope of the use of “group” of the plurality of cell names associated with the electrical functions, or electrical circuit parts, to a “subset” of the plurality of cell names; in other words, the language of the claims do not further limit the group of cell names to a subgroup, or subset of the cell names, but instead, merely states that cell names affiliated with a group of cell names among a plurality of cell names are identified, which lends to the interpretation that the identified **group** of cell names among a plurality of cell names may include **all** of the cell names, i.e. components. Therefore, the examiner respectfully contends that McDonald teaches identifying, among the plurality of cell names, cell names affiliated with the group, in that the parts/components of the circuit to be simulated are identified via being displayed in the “Components” section shown Figure 17 and further recited in column 14, line 61 – column 15, line 10.

7. With respect to claim 10, the applicant submits that McDonald teaches all component are always selected for simulation, without any capability for the user to select which of such components are associated with the group and therefore fails to teach “....a subset of which [virtual buttons] operate to commence testing of parameters of a set of said plurality of electrical functions, the electrical functions included in said set being associated with a group of said plurality of cell names, said plurality of cell names affiliated with said group having a visually perceivable identifier associated therewith”. The examiner respectfully disagrees, in view of the

Art Unit: 2173

arguments presented above. In response to applicant's argument that the references fail to show certain features of applicant's invention, the examiner respectfully notes that the features upon which applicant relies (i.e., capability for the user to select which of the components are associated with the group; choosing one component but not another component within a netlist) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993). As mentioned above, the limitations of the claims, as currently recited, i.e. "...testing of parameters of a set of said plurality of electrical functions, with the electrical functions included in said set being associated with a group of said plurality of cell names", do not limit the scope of the claims to teaching "capability for the user to select which of components are associated with the group" or "choosing one component but not another component within a netlist", since a **group** of components can include **all** of the components. Therefore, the examiner respectfully contends that McDonald teaches a subset of virtual buttons, i.e. button such as "Analyze a Design" and "Simulate", which operate to commence testing of parameters of a set of said plurality of electrical functions, the electrical functions included in said set being associated with a group of said plurality of cell names, i.e. the names of electrical parts/components of the circuit to be simulated, said plurality of cell names affiliated with said group having a visually perceivable identifier associated therewith, i.e. visual indicators displayed with each component/part, as shown in Figure 17 and further recited in column 14, line 61 – column 15, line 10.

Art Unit: 2173

8. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

#### ***Conclusion***

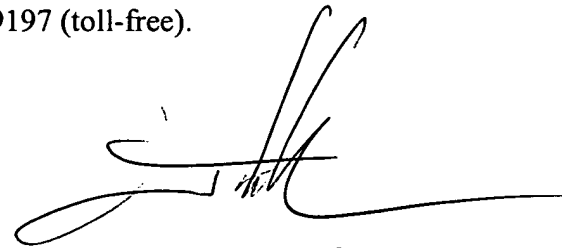
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ting Zhou whose telephone number is (571) 272-4058. The examiner can normally be reached on Monday - Friday 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Cabeca can be reached at (571) 272-4048. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-4058.

Art Unit: 2173

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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A handwritten signature in black ink, appearing to read 'John Cabece', with a long horizontal flourish extending to the right.

**JOHN CABECA**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2100**